



7.0 inch TFT LCD

MODEL NAME: LMIX0700NTN84V2

Date: 2008 / 11 / 20

Customer Signature		
Customer		
Approved Date	Approved By	Reviewed By

Record of Revision

Version	Revise Date	Page	Content
Pre-spec. 01	2007/10/31		Initial release.
Final-spec. 01	2007/12/20		The first version final specification.
		11	Modify Timing Conditions of SYNC mode.
		12	Modify Timing Conditions of DE mode.
Final-spec.02	2008/11/20	6	Update note2 (Modify operating frequency of ADJ signal from "100 ~ 300Hz" to "100Hz ~ 25K Hz").



Contents

- 1. General Specifications 4
- 2. Pin Assignment 5
- 3. Operation Specifications 8
 - 3.1. Absolute Maximum Ratings 8
 - 3.2. Typical Operation Conditions 9
 - 3.3. Power Sequence 10
 - 3.4. Timing Characteristics 11
 - 3.4.1. Timing Conditions 11
 - 3.4.2. Timing Diagram 13
- 4. Optical Specifications 15
- 5. Reliability Test Items 19
- 6. General Precautions 20
 - 6.1. Safety 20
 - 6.2. Handling 20
 - 6.3. Static Electricity 20
 - 6.4. Storage 20
 - 6.5. Cleaning 20
- 7. Mechanical Drawing 21
- 8. Inspection Specifications 22
- 9. Warranty 22
- 10. RMA 22



1. General Specifications

No.	Item	Specification	Remark
1	LCD size	7.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	800X3(RGB)X480	
4	Display mode	Normally white, Transmissive	
5	Dot pitch	0.0635(W)X0.1905(H) mm	
6	Active area	152.4 (W)X91.44 (H) mm	
7	Module size	165(W)X104(H)X5.5(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital (TTL)	
11	Backlight power consumption	2.500W (Typ.)	Note 2
12	Panel power consumption	0.825W (Typ.)	Note 3
13	Weight	130g (Typ.)	

Note 1: Refer to Mechanical Drawing.

Note 2: Including LED Driver power consumption.

Note 3: Including T-con Board power consumption.



2. Pin Assignment

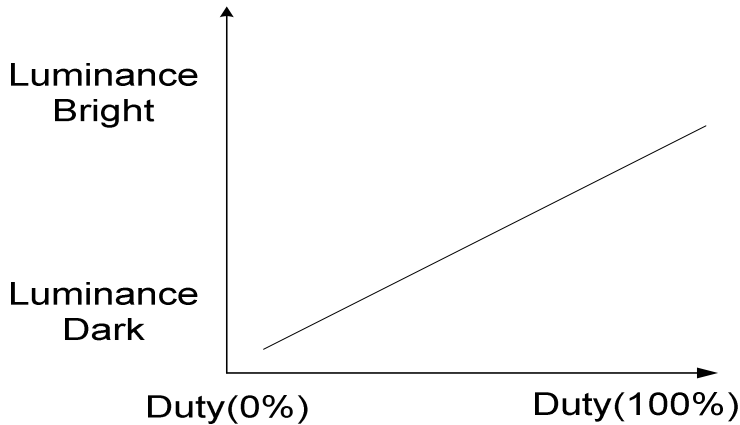
TTL Connector is used for the module electronic interface. The recommended model is “FH33-40S-0.5SH(10)”, manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	V _{LED}	P	Power supply for LED Driver	
2	V _{LED}	P	Power supply for LED Driver	
3	ADJ	I	Adjust the led brightness with PWM Pulse	Note 1,2
4	G _{LED}	P	Ground for LED circuit	
5	G _{LED}	P	Ground for LED circuit	
6	V _{CC}	P	Power supply for digital circuit	
7	V _{CC}	P	Power supply for digital circuit	
8	MODE	I	DE or HV mode control	Note 3
9	DE	I	Data enable	
10	VS	I	Vsync signal input	
11	HS	I	Hsync signal input	
12	GND	P	Power ground	
13	B5	I	Blue data input (MSB)	
14	B4	I	Blue data input	
15	B3	I	Blue data input	
16	GND	P	Power ground	
17	B2	I	Blue data input	
18	B1	I	Blue data input	
19	B0	I	Blue data input(LSB)	
20	GND	P	Power ground	
21	G5	I	Green data input(MSB)	
22	G4	I	Green data input	
23	G3	I	Green data input	
24	GND	P	Power ground	
25	G2	I	Green data input	
26	G1	I	Green data input	

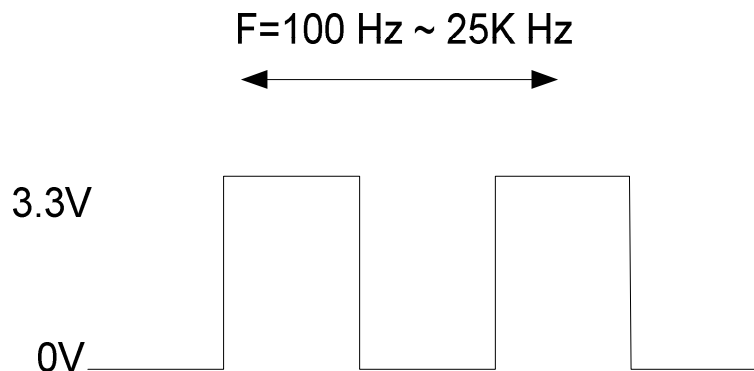


27	G0	I	Green data input(LSB)	
28	GND	P	Power ground	
29	R5	I	Red data input(MSB)	
30	R4	I	Red data input	
31	R3	I	Red data input	
32	GND	P	Power ground	
33	R2	I	Red data input	
34	R1	I	Red data input	
35	R0	I	Red data input(LSB)	
36	GND	P	Power ground	
37	DCLK	I	Sample clock	
38	GND	P	Power ground	
39	L/R	I	Select left or right scanning direction	Note 4,5
40	U/D	I	Select up or down scanning direction	Note 4,5

Note1: Pin3. is used to adjust brightness.



Note 2: ADJ signal=0 ~3.3V; Operating frequency:100 Hz ~ 25K Hz.

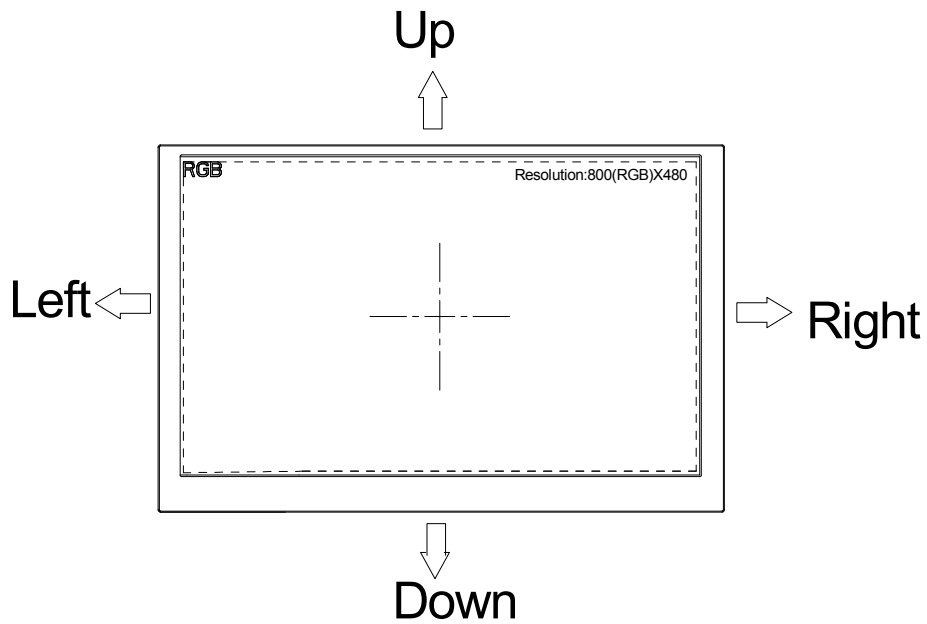


Note 3: DE Mode: Mode="H",HS floating and VS floating.
 HV Mode: Mode="L" and DE floating.

Note 4: Selection of scanning mode

Setting of scan control input		Scanning direction
U/D	L/R	
GND	V _{CC}	Up to down, left to right
V _{CC}	GND	Down to up, right to left
GND	GND	Up to down, right to left
V _{CC}	V _{CC}	Down to up, left to right

Note 5: Scanning direction refer to the figure below.



3. Operation Specifications

3.1. Absolute Maximum Ratings

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V_{CC}	-0.3	6.0	V	
	V_{LED}	-	5.5	V	
Input signal voltage	V_I	-0.3	6.3	V	
Operation Temperature	T_{OP}	-20	70	°C	
Storage Temperature	T_{ST}	-30	80	°C	

Note: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.



3.2. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V_{CC}	3.1	3.3	3.5	V	Note 1
	V_{LED}	4.8	5.0	5.2	V	Note 2
Current consumption	I_{CC}	-	250	300	mA	
	I_{LED}	-	500	550	mA	Note 3
Input logic high voltage	V_{IH}	$0.7V_{CC}$	-	V_{CC}	V	Note 4
Input logic low voltage	V_{IL}	0	-	$0.3V_{CC}$	V	
LED life time	-	20,000	-	-	Hr	Note 5

Note1: V_{CC} setting should match the signals output voltage (refer to Note 4) of customer's system board.

Note 2: LED driving voltage.

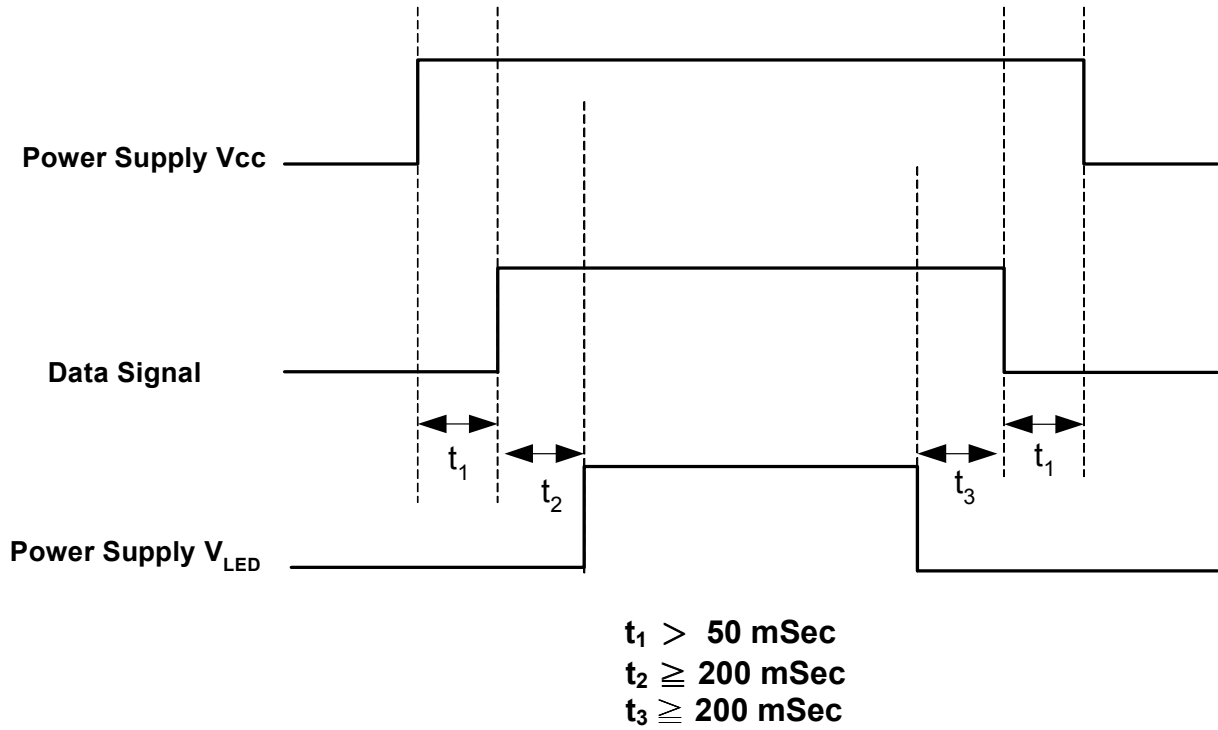
Note 3: LED driving current.

Note 4: DCLK,DE, HS, VS, R0~ R5,,G0~ G5,B0~ B5.

Note 5: The "LED life time" is defined as the module brightness decrease to 50% original brightness at $T_a=25^{\circ}\text{C}$ and $V_{LED}=5.0\text{V}$. The LED lifetime could be decreased if operating V_{LED} is larger than 5.0V.



3.3. Power Sequence



Note: Data Signal includes DCLK, DE, HS, VS, R0~ R5, G0~ G5, B0~ B5.



3.4. Timing Characteristics

3.4.1. Timing Conditions

Input signal characteristics of SYNC mode.

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock Period	t _{CLK}	23.2	25.0	30.7	ns	
Clock Frequency	f _{CLK}	32.4	40	43	MHz	
Clock Low Level Width	t _{WCL}	8	-	-	ns	
Clock High Level Width	t _{WCH}	8	-	-		
Clock Rise/Fall Time	t _{CLKr} , t _{CLKf}	-	-	3		
HSYNC Period	t _{HP}	862	1056	1100	t _{CLK}	
HSYNC Pulse Width	t _{HW}	-	1	-	t _{CLK}	
HSYNC Back Porch	t _{HBP}	-	45	-	t _{CLK}	
HSYNC Width + Back Porch	t _{hw} + t _{HBP}	46			t _{CLK}	
Horizontal valid data width	t _{HV}	800			t _{CLK}	
HSYNC Front Porch	t _{HFP}	t _{HP} - t _{HW} - t _{HBP} - t _{HV}			t _{CLK}	
Horizontal Blank	t _{HBK}	t _{HP} - t _{HV}			t _{CLK}	
VSYNC Period	t _{VP}	628	635	650	t _{HP}	
VSYNC Pulse Width	t _{VW}	-	1	-	t _{HP}	
VSYNC Back Porch	t _{VBP}	22			t _{HP}	
Vertical valid data width	t _v	480			t _{HP}	
Vertical Front Porch	t _{VFP}	t _{VP} - t _{VW} - t _{VBP} - t _v			t _{HP}	
Vertical Blank	t _{VBK}	t _{VP} - t _v			t _{HP}	
Data Setup Time	t _{DS}	5	-	-	ns	
Data Hold Time	t _{DH}	10	-	-	ns	

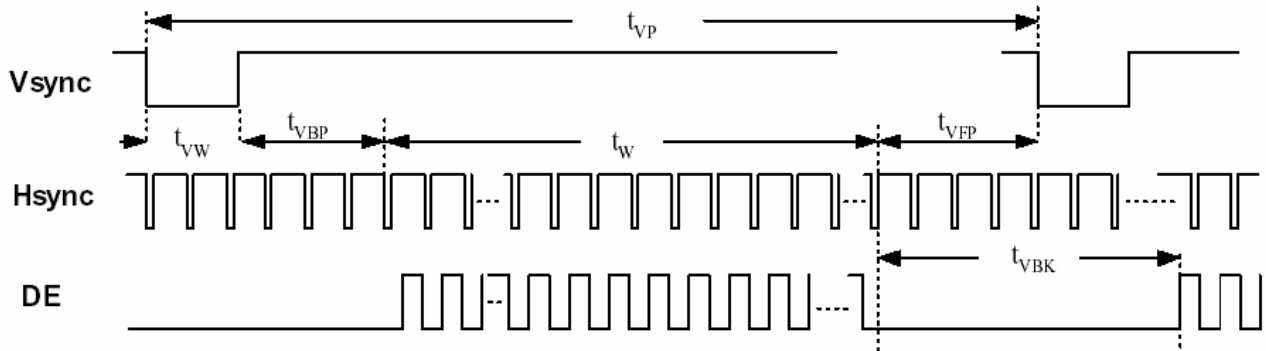


Input signal characteristics of DE mode.

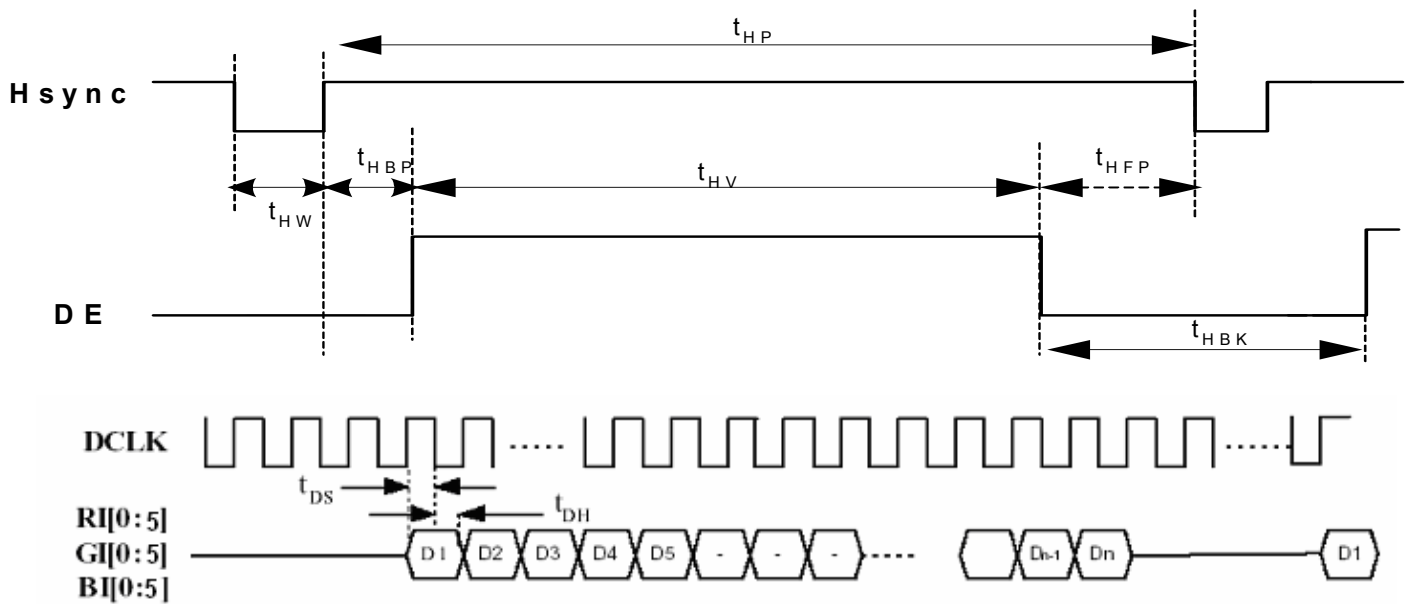
Item		Symbol	Values			Unit	Remark
			Min.	Typ.	Max.		
DCLK	Period	t _{CLK}	23.2	25.0	30.7	ns	
	Frequency	f _{CLK}	32.4	40.0	43.0	MHz	
	Low Level Width	t _{wCL}	6	-	-	ns	
	High Level Width	t _{wCH}	6	-	-		
	Rise/Fall Time	t _{CLKr} , t _{CLKf}	-	-	3		
	Duty	-	0.45	0.50	0.55	-	t _{CLKL} / t _{CLK}
DE	Setup Time	t _{DES}	5	-	-	ns	
	Hold Time	t _{DEH}	10	-	-		
	Rise/Fall Time	t _{DEr} , t _{DEf}	-	-	16		
	Horizontal Period	t _{HP}	862	1056	1100	t _{CLK}	
	Horizontal Valid	t _{HV}	800				
	Horizontal Blank	t _{HBK}	t _{HP} - t _{HV}				
	Vertical Period	t _{VP}	628	635	650	t _{HP}	
	Vertical Valid	t _w	480				
	Vertical Blank	t _{VBK}	t _{VP} - t _w				
DATA	Setup Time	t _{DS}	5	-	-	ns	
	Hold Time	t _{DH}	10	-	-		
	Rise/Fall Time	t _{Dr} , t _{Df}	-	-	3		



3.4.2. Timing Diagram

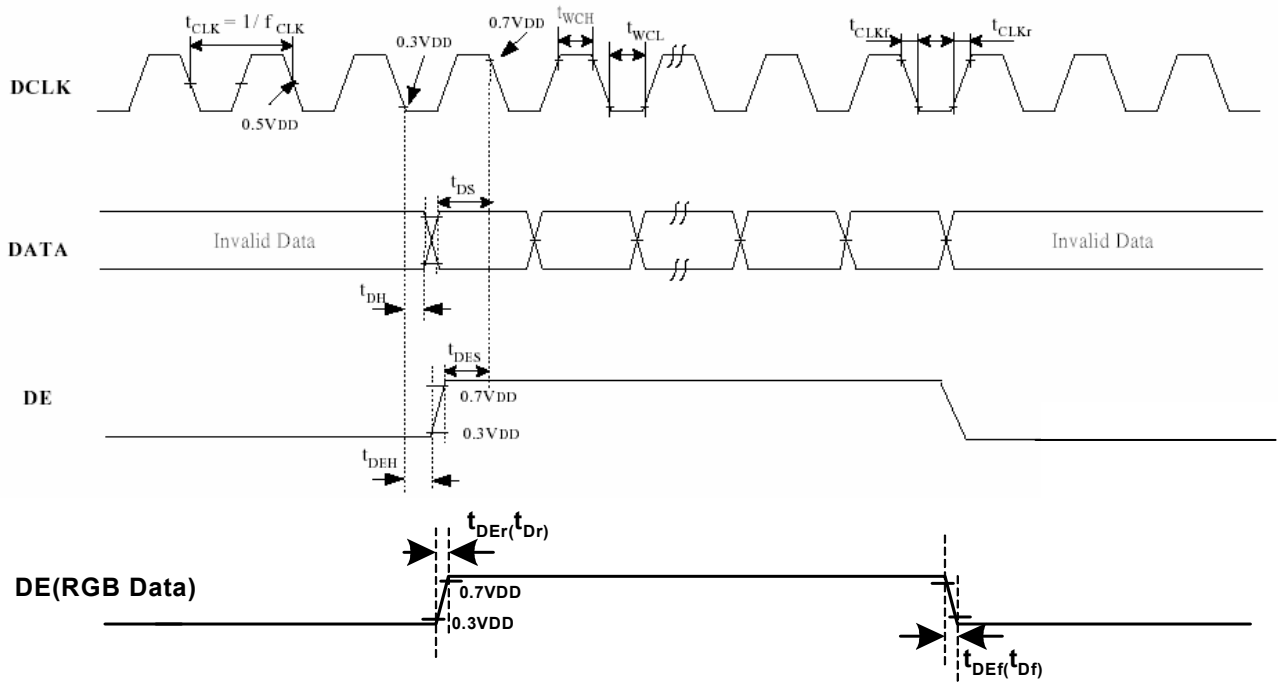


Input Vertical Timing



Input Horizontal Timing





DE and RGB Input Timing



4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	degree	Note 1
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5
	W_Y		0.28	0.33	0.38	-	Note 6
Luminance	L		240	300	-	cd/m ²	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 7

Test Conditions:

1. $V_{CC}=3.3V$, $V_{LED}=5.0V$. The ambient temperature is 25°C.
2. The test systems refer to Note 2.



Note 1: Definition of viewing angle range

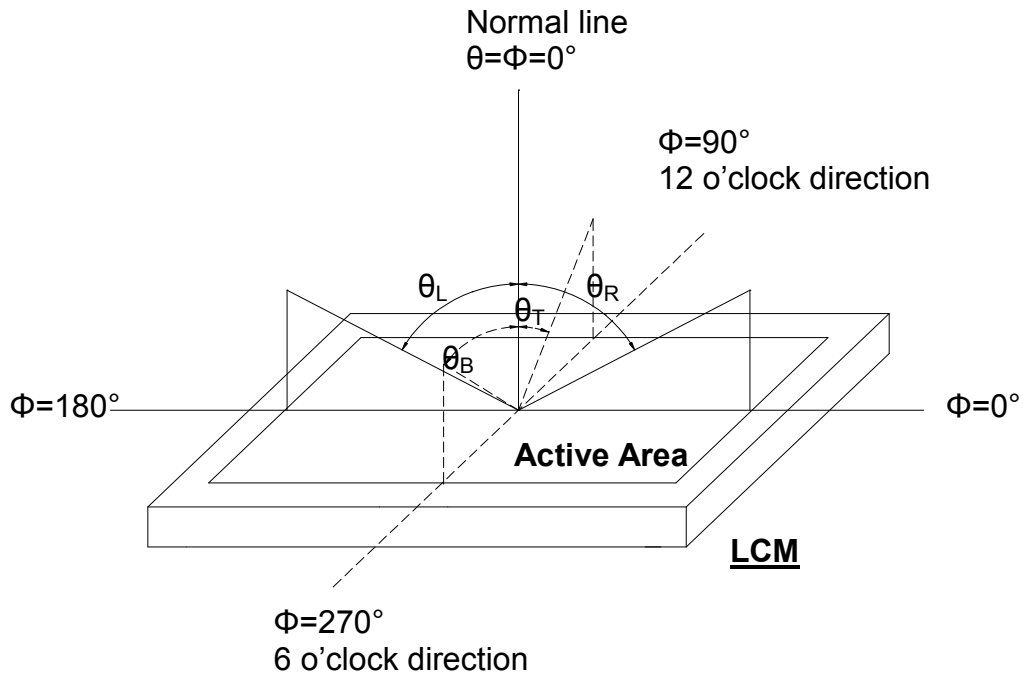


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

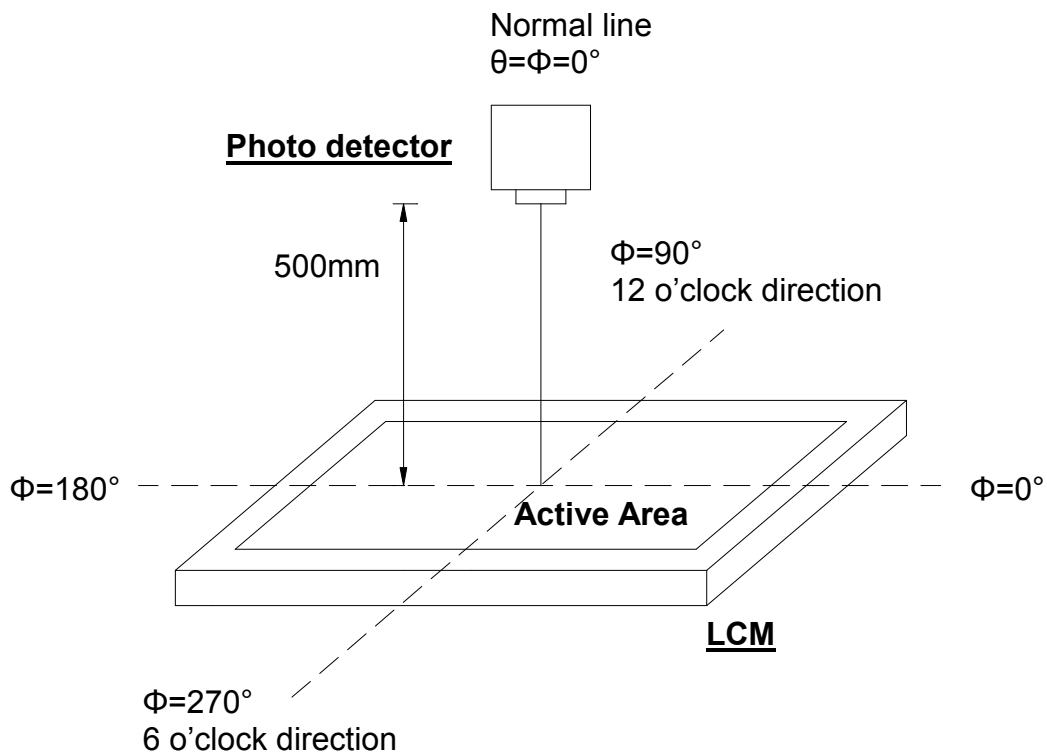


Fig. 4-2 Optical measurement system setup



Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

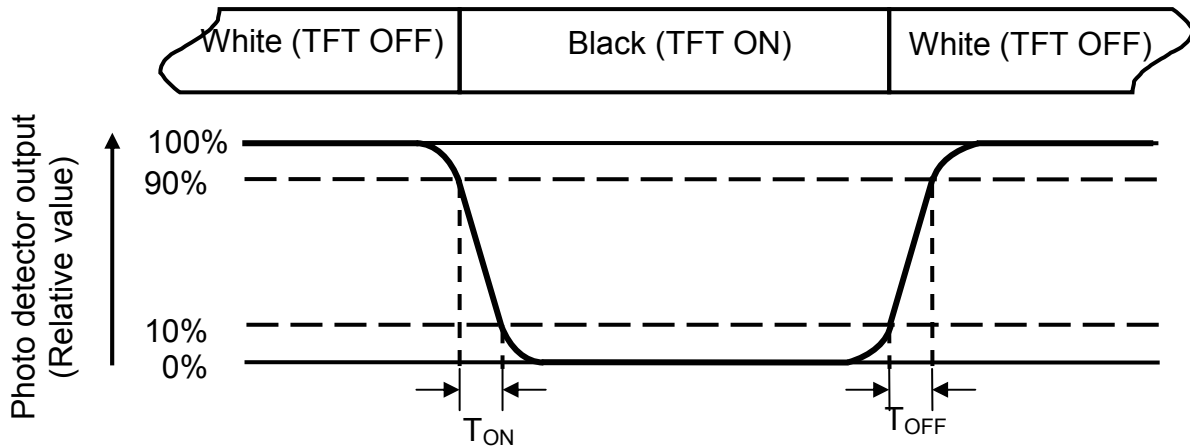


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $V_{LED}=5.0V$.



Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

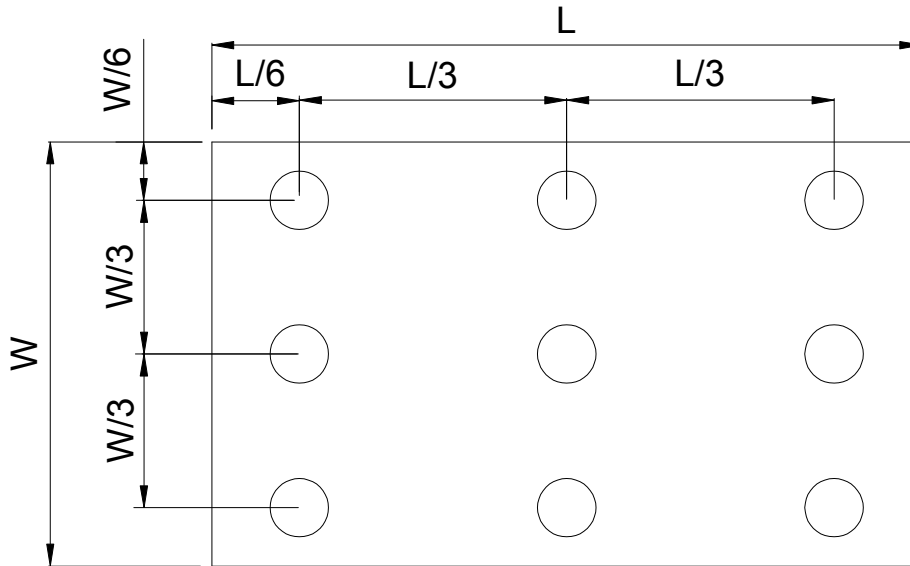


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.
B_{min}: The measured minimum luminance of all measurement position.



6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.



8. Inspection Specifications

The buyer (customer) shall inspect the modules within twenty calendar days since the delivery date (the "inspection period") at its own cost. The results of the inspection (acceptance or rejection) shall be recorded in writing, and a copy of this writing will be promptly sent to the seller.

The buyer may, under commercially reasonable reject procedures, reject an entire lot in the delivery involved if, within the inspection period, such samples of modules within such lot show an unacceptable number of defects in accordance with this incoming inspection standards, provided however that the buyer must notify the seller in writing of any such rejection promptly, and not later than within three business days of the end of the inspection period.

Should the buyer fail to notify the seller within the inspection period, the buyer's right to reject the modules shall be lapsed and the modules shall be deemed to have been accepted by the buyer.

9. Warranty

Inteltronic Inc. warrants to you, the original purchaser, that each of its products will be free from defects in materials and workmanship for one year from the date of purchase.

Inteltronic Inc. will replace or repair any of its module which is found defective electrically or visually when inspected in accordance with Inteltronic Inc. general module inspection standard.

This warranty does not apply to any products which have been on customer's production line, repaired or altered by persons other than repair personnel authorized by Inteltronic Inc., or which have been subject to misuse, abuse, accident or improper installation. Inteltronic Inc. assumes no liability under the terms of this warranty as a consequence of such events.

If an Inteltronic Inc. product is defective, it will be repaired or replaced at no charge during the warranty period. For out-of-warranty repairs, you will be billed according to the cost of replacement materials, service time and freight.

In returning the modules, they must be properly packaged; there should be detailed description of the failures or defect.

Our quality specifications and policies are found in the next pages. Please read thoroughly for details and limitations.

10. RMA

Products purchased through Inteltronic Inc. and under warranty may be returned for replacement.

Contact support@inteltronic.com for RMA number and procedures.



Office Locations



Inteltronic Inc.
www.inteltronicinc.com
Office: 510-471-9900
Fax: 510-471-9901
Address: 29470 Union City Blvd
Union City, CA 94587



www.wahlee.com
Wah Lee Industrial Corp.
HSINCHU OFFICE
18F, No.8, Zihciang S. Rd., Jhubei,
Hsinchu 302, Taiwan, R.O.C.
Tel : 886-3-6205880
FAX: 886-3-6205833

